EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S206	11	"709"/$.ccls. and patch same (subroutine)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/19 07:13
L3	476	"709"/$.ccls. and patch$5 and routine	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/19 07:13
L2	65	"718"/$.ccls. and patch$5 and (routine)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/19 07:05
L1	223	"712"/$.ccls. and patch$5 and (routine)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/19 07:05
S211	196	"712"/$.ccls. and patch and (routine)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/19 07:00
S210	54	"718"/$.ccls. and patch and (routine)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/18 15:56
S209	19	"718"/$.ccls. and patch and (subroutine)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/18 15:36
S208	0	"718"/$.ccls. and (multitask$5 near5 vector) and patch and (subroutine)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/18 15:36
S205	5	"718"/$.ccls. and patch same (subroutine)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/18 15:36
S207	7	"712"/$.ccls. and patch same (subroutine)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/18 15:32

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S204	189	patch same (subroutine)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/18 15:28
S203	0	jmpjsrport	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/18 14:21
S202	15	jmp same jsr same port	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/18 14:21
S189	0	(multitask$5 near5 vector) same patch same (subroutine)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/18 14:21
S201	260	jmp same jsr	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/18 14:19
S200	3909	jmp	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/18 14:19
S199	0	jmp near2 jsr near2 port	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/18 14:18
S198	2	"6260157".pn.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/18 14:18
S197	0	(multi-task$5 near5 vector) and patch and (routine)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/18 14:13
S196	0	(multi-task$5 near5 vector) and patch and (subroutine)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/18 14:13
S195	0	(multi-task$5 near5 vector) same patch same (subroutine)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/18 14:12

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S194	1	(multitask$5 near5 vector) and patch	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/18 14:12
S193	1	(multitask$5 near5 vector) and patch	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/18 14:12
S192	1	(multitask$5 near5 vector) and patch same (routine)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/18 14:12
S191	0	(multitask$5 near5 vector) and patch and (subroutine)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/18 14:10
S190	0	(multitask$5 near5 vector) and patch same (subroutine)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/18 14:10
S188	11	("5202991" \| "5283900" \| "5295265" \| "5721922" \| "5768599" \| "5946487" \| "6061709" \| "6061711" \| "6385637" \| "6430594" \| "6466962").PN.	US-PGPUB; USPAT; USOCR	OR	ON	2006/04/18 14:08
S187	2	"6823517".pn.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/18 12:44
S186	11	(routine program) and (callable) near5 (subprogram subroutine) and (microcontroller)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/18 12:44
S185	1067	(routine program) and (call$7) near5 (subprogram subroutine) and (microcontroller)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/18 12:37
S166	10	routine same (updat$5 revis$5) near7 (vector) and (microcontroller)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/18 12:36
S184	12	(multitask$5) adj (control) adj (program)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/07 10:23

EAST Search History

S183	149	(multitask$5) near5 (control) near5 (program)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/07 10:23
S182	2	"4530051".pn.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/07 10:22
S173	682	S169 and (out-of-order) near6 (execut$5) near5 (process$5)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/07 09:58
S181	2	"6260157".pn.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/07 09:37
S180	7	S169 and (out-of-order) near6 (execut$5) near5 (process$5) and (multitask$5)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/07 09:37
S179	0	(out-of-order) near6 (execut$5) near5 (process$5) same (multitask$5)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/07 08:49
S178	0	(out-of-order) near6 (execut$5) near5 (process$5) same (multitask$5)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/07 08:49
S177	0	S169 and (out-of-order) near6 (execut$5) near5 (process$5) same (multitask$5)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/07 08:49
S176	3	S169 and (out-of-order) near6 (execut$5) near5 (process$5) same (microcontroller)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/07 08:47
S174	0	S169 and (out-of-order) near6 (execut$5) near5 (process$5) same (vector) same (microcontroller)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/07 08:47
S175	3	S169 and (out-of-order) near6 (execut$5) near5 (process$5) same (vector)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/07 08:45

EAST Search History

S172	839	S169 and (simultaneous out-of-order) near6 (execut$5) near5 (process$5)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/07 08:06
S171	0	S169 and (simultaneous out-of-order) near6 (execut$5) near5 (process$5) and (updat$5) near6 (vector pointer) near5 (wait$5 acknowledge) near5 (output result outcome decision)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/07 08:06
S170	0	S169 and (microcontroller) and (dynamic$5) near5 (patch near3 routine)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/07 08:04
S169	12198	"712"/$.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/07 08:04
S156	2	(microcontroller) and (dynamic$5) near5 (patch near3 routine)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/07 08:04
S168	27	(updat$5 revis$5) near7 (vector) and (microcontroller) and (wait) near4 (ouput result)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/07 08:03
S167	326	(updat$5 revis$5) near7 (vector) and (microcontroller)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/07 08:00
S165	137	routine same (updat$5 revis$5) near7 (vector)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/07 07:57
S158	118	routine same (wait) near4 (ouput result)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/07 07:56
S164	14	(microcontroller) same (logical decision) near4 (node point) near5 (decid$5 undecid$5 reach$5)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/07 07:52
S163	15	(sequent$5 subsequent$5) same (microcontroller) same (logical decision) near4 (node point)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/07 07:22

EAST Search History

S162	19	(sequent$5 subsequent$5) same (microcontroller) and (patch near3 routine)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/07 07:18
S131	1	(sequent$5 subsequent$5) near4 (execut$5) and (microcontroller) and (patch near3 routine)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/07 07:13
S161	33	(sequent$5 subsequent$5) near4 (execut$5) same (microcontroller) same(routine)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/07 07:02
S160	691	(sequent$5 subsequent$5) near4 (execut$5) and (microcontroller) and (routine)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/07 07:02
S159	691	(sequent$5 subsequent$5) near4 (execut$5) and (microcontroller) and (routine)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/07 07:01
S157	2	"6182238".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/04 15:07
S155	2	"5127096".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/04 14:58
S146	2	"4530051".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/04 14:31
S154	0	(main adj routine) near4 subroutine same task near5 vector	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/04 14:29
S153	13	(main adj routine) near4 subroutine same vector	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/04 14:29
S150	1399	(main adj routine) near4 subroutine	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/04 14:27
S152	22	(main near2 routine) same subroutine same microcontroller	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/04 14:24

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S147	3185	(main near2 routine) same subroutine	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/04 14:24
S151	5	(main adj routine) near4 subroutine same microcontroller	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/04 14:22
S149	1526	(main near2 routine) near4 subroutine	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/04 14:22
S148	3212	(main near2 routine) same subroutine	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/04 14:22
S145	69	(out near3 order near3 execut$5) same (vector)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/04 13:57
S144	1	(multitask$5) near5 (vector)same (microcontroller)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/04 13:57
S143	16	(out near3 order near3 execut$5) same (microcontroller)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/04 13:57
S142	0	(out near3 order near3 execut$5) same (data near3 dependen$5) same (microcontroller)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/04 13:52
S141	4620	(out near3 order near3 execut$5)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/04 13:50
S140	8	(Multitasking) near3 vector	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/04 13:50
S139	12	(stall) same ((data) near4 (dependenc$5)) and (microcontroller)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/04 13:47

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S138	357	(stall) same ((data) near4 (dependenc$5))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/04 13:37
S137	1	(stall) same (data near4 dependanc$5)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/04 13:33
S136	0	(stall) same (data near4 dependanc$5)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/04 13:32
S135	6	("5465106" \| "6286070" \| "6363423" \| "6496858" \| "6523083" \| "6535924").PN.	US-PGPUB; USPAT; USOCR	OR	ON	2005/11/01 15:00
S134	47	(network adj interface adj card) same (microcontroller)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/01 14:02
S133	13	(network adj interface adj card) same (real near3 time) and (microcontroller)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/01 14:02
S132	0	((network adj interface adj card) same (physical near4 interface)) same (real near3 time) and (microcontroller)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/01 13:50
S130	27	((network adj interface adj card) (physical near4 interface)) same (real near3 time) and (microcontroller)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/01 13:50
S129	76	(sequent$5 subsequent$5) near4 (execut$5) near4 (routin$5) and (microcontroller)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/01 13:21
S128	3	712/247,245,243,233.ccls. and (network adj interface adj card)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/01 12:53
S23	3	(712/247,245,243,233.ccls. and @ad<"20001115") and (network adj interface adj card)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/01 12:53
S127	2	"5127096".pn.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/03 09:51
S126	2	"6182238".pn.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/03 09:51

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S125	6	"718"/$.ccls. and (wait$5) near4 (outcome result state) near5 (subroutine subprogram subprocedure subprocess sub-routine sub-program sub-procedure sub-process)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/03 09:27
S124	0	"709"/$.ccls. and (wait$5) near4 (outcome result state) near5 (subroutine subprogram subprocedure subprocess sub-routine sub-program sub-procedure sub-process)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/03 08:53
S119	46	(wait$5) near4 (outcome result state) near5 (subroutine subprogram subprocedure subprocess sub-routine sub-program sub-procedure sub-process)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/03 08:52
S123	2	"4530051".pn.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/03 08:38
S122	183	(sequential) near8 (process$5 execut$5) near5 (subroutine subprogram subprocedure subprocess sub-routine sub-program sub-procedure sub-process)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/03 08:38
S121	0	(sequential) near8 (process$5 execut$5) near5 (subroutine subprogram subprocedure subprocess sub-routine sub-program sub-procedure sub-process) near5 (decision) near3 (node point)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/03 08:05
S120	0	(sequential) near8 (subroutine subprogram subprocedure subprocess sub-routine sub-program sub-procedure sub-process) near5 (decision) near3 (node point)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/03 08:04
S118	396	(wait$5) near4 (outcome result state) same(subroutine subprogram subprocedure subprocess sub-routine sub-program sub-procedure sub-process)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/02 16:53
S117	0	(wait$5) near4 (state) near4(subroutine subprogram subprocedure subprocess sub-routine sub-program sub-procedure sub-process) same(assembl$5 consolidat$5)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/02 16:51
S116	99	(main) near3 (routine program procedure process) near4(subroutine subprogram subprocedure subprocess sub-routine sub-program sub-procedure sub-process) same(assembl$5 consolidat$5)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/02 16:50
S115	54954	(main) near3 (routine program procedure process) near4(subroutine subprogram subprocedure subprocess sub-routine sub-program sub-procedure sub-process) same(assembl$5 consolidat$5) near4 (outcome result) nerar4 (subroutine subprogram subprocedure subprocess sub-routine sub-program sub-procedure sub-process)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/02 16:50

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S114	54954	(main) near3 (routine program procedure process) near4(subroutine subprogram subprocedure subprocess sub-routine sub-program sub-procedure sub-process) and (assembl$5 consolidat$5) near4 (outcome result) nerar4 (subroutine subprogram subprocedure subprocess sub-routine sub-program sub-procedure sub-process)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/02 16:49
S113	39	(main) near3 (routine program procedure process) near5 (execut$5 process$5) near3 (multiple several plural$5) near4(subroutine subprogram subprocedure subprocess sub-routine sub-program sub-procedure sub-process)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/02 16:47
S111	786	(routine program procedure process) near5 (execut$5 process$5) near3 (multiple several plural$5) near4(subroutine subprogram subprocedure subprocess sub-routine sub-program sub-procedure sub-process)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/02 16:40
S112	4145	(main) near3 (routine program procedure process) near3(subroutine subprogram subprocedure subprocess sub-routine sub-program sub-procedure sub-process)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/02 16:39
S107	38	(main) near3 (routine program procedure process) near3(subroutine subprogram subprocedure subprocess sub-routine sub-program sub-procedure sub-process) and (sequential near3 (controller processor CPU microcontroller))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/02 16:39
S110	2	(routine program procedure process) near5 (execut$5 process$5) near3 (multiple several plural$5) near4(subroutine subprogram subprocedure subprocess sub-routine sub-program sub-procedure sub-process) and (sequential near3 (controller processor CPU microcontroller))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/02 16:38
S109	1	(main) near3 (routine program procedure process) near5 (execut$5 process$5) near3 (multiple several plural$5) near4(subroutine subprogram subprocedure subprocess sub-routine sub-program sub-procedure sub-process) and (sequential near3 (controller processor CPU microcontroller))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/02 16:37
S108	5	(execut$5 process$5) near3 (multiple several plural$5) near4 (subroutine subprogram subprocedure subprocess sub-routine sub-program sub-procedure sub-process) and (sequential near3 (controller processor CPU microcontroller))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/02 16:32
S106	474	(main) near3 (routine program procedure process) near3(subroutine subprogram subprocedure subprocess sub-routine sub-program sub-procedure sub-process) and (sequen$6 near3 (controller processor CPU microcontroller))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/02 16:24
S92	61	(main) near3 (routine program procedure process) near3(subroutine subprogram subprocedure subprocess sub-routine sub-program sub-procedure sub-process) same (mak$5 execut$5 call$5) near4 (subsequent$5 sequenti$5)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/02 16:23
S105	0	(network adj interface adj card) and microcontroller) and (((routine$2 subroutine$3) near6 (decision near2 (point node)))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/02 16:05

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S104	0	(network adj interface adj card) and microcontroller) and (((routine$2 subroutine$3) near6 (deci$6 near2 (point node)))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/02 16:05
S103	301	((network adj interface adj card) and microcontroller) and ((routine$2 subroutine$3) and (RAM ROM Vector$3))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/02 16:03
S7	70	(((network adj interface adj card) and @ad<"20001115") and microcontroller) and ((routine$2 subroutine$3) and (RAM ROM Vector$3))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/02 16:01
S102	2	"6778826".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/02 15:51
S101	110	(network adj interface) and (main) near3(routine program procedure process) near3(subroutine subprogram subprocedure subprocess sub-routine sub-program sub-procedure sub-process) and (RAM ROM Vector$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/02 15:51
S100	16	(network adj interface adj card) and (main) near3(routine program procedure process) near3(subroutine subprogram subprocedure subprocess sub-routine sub-program sub-procedure sub-process) and ((routine$2 subroutine$3) and (RAM ROM Vector$3))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/02 15:29
S97	0	(network adj interface adj card) and (main) near3(routine program procedure process) near3(subroutine subprogram subprocedure subprocess sub-routine sub-program sub-procedure sub-process) and (mak$5 execut$5 call$5) near4 (subsequent$5 sequenti$5) and ((routine$2 subroutine$3) and (RAM ROM Vector$3))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/02 15:28
S99	0	(network adj interface adj card) and (main) near3(routine program procedure process) near3(subroutine subprogram subprocedure subprocess sub-routine sub-program sub-procedure sub-process) and (mak$5 execut$5 call$5) near4 (subsequent$5 sequenti$5)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/02 15:27
S98	0	(network adj interface adj card) and (main) near3(routine program procedure process) near3(subroutine subprogram subprocedure subprocess sub-routine sub-program sub-procedure sub-process) and (mak$5 execut$5 call$5) near4 (subsequent$5 sequenti$5) and(RAM ROM Vector$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/02 15:27
S6	615	((network adj interface adj card) and @ad<"20001115") and ((routine$2 subroutine$3) and (RAM ROM Vector$3))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/02 15:26
S96	2	"5127096".pn.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/02 15:09
S95	2	(Rohit near2 Chandra) and (Anoop near3 Gupta)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/02 15:09

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S94	2	"5404521".pn.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/02 13:22
S93	2	"5404521".pn.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/02 13:21
S91	740	(main) near3(routine program procedure process) near3(subroutine subprogram subprocedure subprocess sub-routine sub-program sub-procedure sub-process) and (mak$5 execut$5 call$5) near4 (subsequent$5 sequenti$5)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/02 13:17
S3	0	"718"/$.ccls. and ((ROM (Read adj only adj memory))same(RAM (random adj access adj memory))) same (Microcontroller))same ((routine (main adj (routine program application))near10 (subroutine))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/09/16 12:16
S2	0	"709"/$.ccls. and ((ROM (Read adj only adj memory))same(RAM (random adj access adj memory))) same (Microcontroller))same ((routine (main adj (routine program application))near10 (subroutine))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/09/16 12:16
S1	112	((ROM (Read adj only adj memory))same(RAM (random adj access adj memory))) same (Microcontroller))same ((routine (main adj (routine program application))near10 (subroutine))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/09/16 12:15
S90	0	(network adj interface adj card)and(control)adj (program)same ((call$3 invoke invoca$5)near10(routine subroutine))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/09/16 10:58
S89	54	(network adj interface adj card)and(control)adj (program)and ((call$3 invoke invoca$5)near10(routine subroutine))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/09/16 10:58
S88	0	(in-system adj program$4) and (network adj interface adj card)and(control)adj (program)and ((call$3 invoke invoca$5)near10(routine subroutine))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/09/16 10:58
S54	0	((in-system adj program$4) and @ad<"20001115") and ((((network adj interface adj card)and((control)adj (program))) and ((call$3 invoke invoca$5)near10(routine subroutine)))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/09/16 10:56
S87	1	((updat$5)near5 vector) and (main adj (routine)) same (sub adj routine)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/09/16 10:55
S86	1	((updat$5 refresh$5 reset$5)near5 vector) and (main adj (routine)) same (sub adj routine)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/09/16 10:55

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S85	0	(multitask$5 near5 vector) and (main adj (routine)) same (sub adj routine)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/09/16 10:53
S83	460	(main adj (routine)) same (sub adj routine)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/09/16 10:52
S84	2	((ROM)same(RAM) same(Microcontroller)) and (main adj (routine)) same (sub adj routine)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/09/16 10:50
S82	0	((main adj (routine)) same (sub adj routine)) and (call$5 order$5 instruct$5 provid$5) near10 (sub adj routine) same (reset$5 updat$5) near10 (vector)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/09/16 10:49
S81	0	(Embedded adj microcontroller) and (main adj (routine)) same (sub adj routine)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/09/16 10:48
S73	0	(NIC (network adj interface adj card)) and ((call$5 order$5 instruct$5 provid$5) near10 (sub adj routine) same (reset$5 updat$5) near10 (vector))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/09/16 10:48
S80	151	(Embedded adj microcontroller) and(routine (main adj (routine)) same (sub adj routine))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/09/16 10:47
S75	112	((ROM (Read adj only adj memory))same(RAM (random adj access adj memory))) same (Microcontroller))same ((routine (main adj (routine program application)) same (sub adj routine))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/09/16 10:45
S79	81	(Embedded adj microcontroller) and ((ROM (Read adj only adj memory))and (RAM (random adj access adj memory)))) and ((routine (main adj (routine program application)) same (sub adj routine))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/09/16 10:44
S78	81	(Embedded adj microcontroller) and ((ROM (Read adj only adj memory))and (RAM (random adj access adj memory))) and (Microcontroller)) and ((routine (main adj (routine program application)) same (sub adj routine))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/09/16 10:44
S77	530	Embedded adj microcontroller	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/09/16 10:44
S63	3785	((ROM (Read adj only adj memory))and (RAM (random adj access adj memory))) and (Microcontroller)) and ((routine (main adj (routine program application)) same (metaroutine subroutine subprogram))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/09/16 10:44
S76	4	(ROM)same(RAM) same (Microcontroller)same ((routine) same (sub adj routine))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/09/16 10:43

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S52	286	(Embedded adj microcontroller) and @ad<"20001115"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/09/16 10:43
S68	112	((ROM (Read adj only adj memory))same(RAM (random adj access adj memory))) same (Microcontroller))same ((routine (main adj (routine program application)) same (subroutine))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/09/16 10:41
S74	1	(Microcontroller) and ((call$5 order$5 instruct$5 provid$5) near10 (sub adj routine) same (reset$5 updat$5) near10 (vector))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/09/16 10:40
S72	0	((((ROM (Read adj only adj memory))same(RAM (random adj access adj memory))) and (Microcontroller)) and ((routine (main adj (routine program application)) same (sub adj routine))) and (NIC (network adj interface adj card)) and (call$5 order$5 instruct$5 provid$5) near10 (sub adj routine) and (reset$5 updat$5) near10 (vector)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/09/16 10:38
S67	3782	((ROM (Read adj only adj memory))and (RAM (random adj access adj memory))) and (Microcontroller)) and ((routine (main adj (routine program application)) same (subroutine))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/09/16 10:37
S64	3152	((ROM (Read adj only adj memory))same(RAM (random adj access adj memory))) and (Microcontroller)) and ((routine (main adj (routine program application)) same (metaroutine subroutine subprogram))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/09/16 10:32
S71	112	((ROM (Read adj only adj memory))same(RAM (random adj access adj memory))) same (Microcontroller))same ((routine (main adj (routine))and (sub-routine))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/09/16 09:52
S70	112	((ROM (Read adj only adj memory))same(RAM (random adj access adj memory))) same (Microcontroller))same ((routine (main adj (routine))and (subroutine))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/09/16 09:52
S69	112	((ROM (Read adj only adj memory))same(RAM (random adj access adj memory))) same (Microcontroller))same ((routine (main adj (routine))near10 (subroutine))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/09/16 09:51
S66	112	((ROM (Read adj only adj memory))same(RAM (random adj access adj memory))) same(Microcontroller)) same((routine (main adj (routine program application)) same (metaroutine subroutine subprogram))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/09/16 09:45
S65	1468	((ROM (Read adj only adj memory))same(RAM (random adj access adj memory))) same(Microcontroller)) and ((routine (main adj (routine program application)) same (metaroutine subroutine subprogram))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/09/16 09:45
S32	1	(((Patch$2 reprogram repair)adj(routine$2 subroutine$3))) and (network adj interface adj card)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/09/16 09:39
S62	2	"4530051".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2003/12/22 12:14

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S61	2	"6182238".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2003/12/22 12:14
S60	294	324/415,416.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2003/12/22 12:13
S59	135	324/376,377.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2003/12/22 11:06
S58	108035	"324"/$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2003/12/22 11:04
S39	108035	"324"/$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2003/12/22 10:32
S57	8	((in-system adj program$4) and @ad<"20001115") and "711"/$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/12/22 08:34
S56	1	(((in-system adj program$4) and @ad<"20001115") and ((((control)adj (program))) and ((call$3 invoke invoca$5)near10(routine subroutine)))) and "711"/$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/12/22 08:33
S55	2	((in-system adj program$4) and @ad<"20001115") and ((((control)adj (program))) and ((call$3 invoke invoca$5)near10(routine subroutine)))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/12/22 08:33
S53	173	(in-system adj program$4) and @ad<"20001115"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2003/12/22 08:26
S51	23	(((network adj interface adj card) and @ad<"20001115") and((control)adj (program))) and ((call$3 invoke invoca$5)near10(routine subroutine))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/12/22 08:26
S44	78	(in-circuit adj program$4) and @ad<"20001115"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2003/12/22 08:25
S50	114	((network adj interface adj card) and @ad<"20001115") and((control)adj (program))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/12/18 14:17
S49	0	((network adj interface adj card) and @ad<"20001115") and ((Multitask$4) Near10 ((control)adj (program)))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/12/18 14:16

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S48	2345	(network adj interface adj card) and @ad<"20001115"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2003/12/18 14:15
S47	27	((in-circuit adj program$4) and @ad<"20001115") and microcontroller	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/12/18 13:49
S46	3	(((in-circuit adj program$4) and @ad<"20001115") and microcontroller) and ((Embedded adj microcontroller) and @ad<"20001115")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2003/12/18 13:49
S45	3	(((in-circuit adj program$4) and @ad<"20001115") and microcontroller) and ((Embedded adj microcontroller) and @ad<"20001115") and (call$3 near (routine subroutine))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2003/12/18 13:46
S37	31	((Embedded adj microcontroller) and @ad<"20001115") and (call$3 near (routine subroutine function))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2003/12/18 13:46
S43	115	in-circuit adj program$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2003/12/18 13:45
S42	0	in-circuit adj program$%	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2003/12/18 13:45
S35	560	Embedded adj microcontroller	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/12/18 13:45
S5	2345	(network adj interface adj card) and @ad<"20001115"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2003/12/18 13:45
S41	2	"4530051".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2003/12/18 13:44
S38	37	((Embedded adj microcontroller) and @ad<"20001115") and (call$3 near3 (routine subroutine function))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2003/12/18 13:43
S40	12	Sun-albert-c.in.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2003/12/18 12:30
S36	1	((Embedded adj microcontroller) and @ad<"20001115") and network adj interface adj card	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2003/12/18 08:18

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S4	5119	network adj interface adj card	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2003/12/18 08:18
S34	1804	(((network adj interface adj card) NIC "NIC") and (controller)) and @ad<"20001115"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2003/12/18 08:16
S33	3491	((network adj interface adj card) NIC "NIC") and (controller)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2003/12/17 16:44
S13	360	(physical adj layer adj interface) and @ad<"20001115"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2003/12/17 16:44
S31	254	((Patch$2 reprogram repair)adj(routine$2 subroutine$3))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/12/17 16:41
S30	2	(((network adj interface adj card) and @ad<"20001115") and ((routine$2 subroutine$3) and (RAM ROM Vector$3))) and ((Patch$2 reprogram repair)near3(routine$2 subroutine$3))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/12/17 16:41
S29	5	Programmable near2 (network adj (interface$3 adapter$3)adj card)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2003/12/17 16:17
S28	1602	Programmable and (network adj (interface$3 adapter$3)adj card)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2003/12/17 16:17
S27	163	((network adj interface adj card) and @ad<"20001115") and microcontroller	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2003/12/17 16:16
S26	4	(Multitasking) adj (control)adj (program)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/12/17 16:13
S25	2	(Multi$4 adj (task routine) adj (control)adj (program))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/12/17 16:13
S24	0	(((network adj interface adj card) and @ad<"20001115") and microcontroller) and (Multi$4 adj (task routine) adj (control)adj (program))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/12/17 16:07
S22	798	712/247,245,243,233.ccls. and @ad<"20001115"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2003/12/17 16:04

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S21	874	712/247,245,243,233.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2003/12/17 16:03
S20	14	(((network adj interface adj card) and @ad<"20001115") and microcontroller) and ((upgrad$4 patch$3 reprogram$5) and (state adj machine))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2003/12/17 16:03
S19	0	(((network adj interface adj card) and @ad<"20001115") and microcontroller) and (physical adj layer adj interface)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2003/12/17 15:45
S12	596	physical adj layer adj interface	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2003/12/17 15:45
S18	17	(((physical adj layer adj interface) and @ad<"20001115") and ((Control$4 monitor$4)and (function state status))) and microcontroller	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2003/12/17 15:35
S16	9	(((physical adj layer adj interface) and @ad<"20001115") and ((routine$2 subroutine$3) and (RAM ROM Vector$3))) and microcontroller	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2003/12/17 15:34
S17	295	((physical adj layer adj interface) and @ad<"20001115") and ((Control$4 monitor$4)and (function state status))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2003/12/17 15:23
S15	44	((physical adj layer adj interface) and @ad<"20001115") and ((routine$2 subroutine$3) and (RAM ROM Vector$3))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/12/17 15:11
S14	0	((physical adj layer adj interface) and @ad<"20001115") and ((routine$2 and subroutine$3) and (RAM ROM Vector$3))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/12/17 15:11
S8	10	(((network adj interface adj card) and @ad<"20001115") and microcontroller) and ((routine$2 and subroutine$3) and (RAM ROM Vector$3))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/12/17 15:11
S11	33	((((network adj interface adj card) and @ad<"20001115") and ((routine$2 subroutine$3) and (RAM ROM Vector$3))) and (multitask$4 and control$4)) and ((routine$2 subroutine$3) and (RAM ROM Vector$3))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/12/17 14:55
S10	2	(((((network adj interface adj card) and @ad<"20001115") and ((routine$2 subroutine$3) and (RAM ROM Vector$3))) and (multitask$4 and control$4)) and ((routine$2 subroutine$3) and (RAM ROM Vector$3))) and microcontroller	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2003/12/17 14:51
S9	33	(((network adj interface adj card) and @ad<"20001115") and ((routine$2 subroutine$3) and (RAM ROM Vector$3))) and (multitask$4 and control$4)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/12/17 14:49

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 Digital Object Identifier 10.1109/TAP.2003.813611
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5. **Using area-average remotely sensed surface soil moisture in multipatch l assimilation systems**
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Alan Cox
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2 A powerful microprogram control unit - the 6700
Clive Ghest
September 1975 **Proceedings of the 8th annual workshop on Microprogramming**
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3 Rapid prototyping of microprocessor-based systems
Raj S. Mitra, Biswarooop Guha, Anupam Basu
November 1993 **Proceedings of the 1993 IEEE/ACM international conference on Computer-aided design**
Publisher: IEEE Computer Society Press
Full text available: pdf(418.02 KB) Additional Information: full citation, references

4 Synthesis fo the hardware/software interface in microcontroller-based systems
Pai Chou, Ross Ortega, Gaetano Borriello
November 1992 **Proceedings of the 1992 IEEE/ACM international conference on Computer-aided design**

Publisher: IEEE Computer Society Press
Full text available: pdf(850.99 KB) Additional Information: full citation, references, citings, index terms

5 An Object-Oriented Communication Library for Hardware-Software CoDesign
Frank Vahid, Linus Tauro
March 1997 **Proceedings of the 5th International Workshop on Hardware/Software Co-Design**
Publisher: IEEE Computer Society
Full text available: pdf(858.92 KB) Publisher Site Additional Information: full citation, abstract, citings

Implementing communication between hardware and software components can be a time-consuming task. Numerous communication protocols are available, differing greatly in their implementation details. Designers must spend much time focusing on those details. Even when libraries are available to encapsulate communication into C or VHDL routines, these routines are not consistent across protocols, making it difficult to switch to other protocols. In this paper, we propose an object-oriented communicat ...

Keywords: Communication, Libraries, Object-Oriented, C, VHDL, Codesign

6 Exploiting FPGA-features during the emulation of a fast reactive embedded system
Karlheinz Weiß, Thorsten Steckstor, Gernot Koch, Wolfgang Rosenstiel
February 1999 **Proceedings of the 1999 ACM/SIGDA seventh international symposium on Field programmable gate arrays**
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Nick Bailey
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An Embedded Systems Project: CREAT is a tool set for teaching embedded systems. In designing it, Mr. Bailey wanted it to be useful for real problems, cheap enough to build on the pittance which is an undergraduate's project budget, and totally open<

8 Interface co-synthesis techniques for embedded systems
Pai Chou, Ross B. Ortega, Gaetano Borriello
December 1995 **Proceedings of the 1995 IEEE/ACM international conference on Computer-aided design**
Publisher: IEEE Computer Society
Full text available: pdf(175.83 KB) Publisher Site Additional Information: full citation, abstract, references, citings, index terms

Abstract: A key aspect of the synthesis of embedded systems is the automatic integration of system components. This entails the derivation of both the hardware and software interfaces that will bind these elements together and permit them to communicate correctly and efficiently. Without the automatic synthesis of these interfaces, designers are not able to fully simulate and evaluate their systems. Frequently, they are discouraged from exploring the design space of different hardware/software p ...

Keywords: CAD, computer interfaces, design cycle, design space exploration tools, embedded systems, glue logic, hardware/software interfaces, real-time systems, synthesis

9 The Chinook hardware/software co-synthesis system

Pai H. Chou, Ross B. Ortega, Gaetano Borriello

September 1995 **Proceedings of the 8th international symposium on System synthesis**

Publisher: ACM Press

Full text available: pdf(79.55 KB) Publisher Site

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Abstract: Designers of embedded systems are facing ever tighter constraints on design time, but computer-aided design tools for embedded systems have not kept pace with these trends. The Chinook co-synthesis system addresses the automation of the most time-consuming and error-prone tasks in embedded controller design, namely the synthesis of interface hardware and software needed to integrate system components, the migration of functions between processors or custom logic, and the co-simulation ...

Keywords: Chinook hardware/software co-synthesis system, computer-aided design tools, custom logic, design co-simulation, design time constraints, embedded controller design, error-prone tasks, function migration, interface hardware, interface software, logic CAD, logic design, microcontrollers, microprocessors, real-time systems, software tools, system components integration

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13 The Graft-Host method for design change

Guillermo Arango, Eric Schoen, Robert Pettengill, Josiah Hoskins

May 1993 **Proceedings of the 15th international conference on Software Engineering**

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14 Constructing application-specific heterogeneous embedded architectures from custom HW/SW applications
Steven Vercauteren, Bill Lin, Hugo De Man
June 1996 **Proceedings of the 33rd annual conference on Design automation**
Publisher: ACM Press
Full text available: pdf(97.89 KB) Additional Information: full citation, references, citings, index terms

15 Experiencing the codesign process: Microcomputer Systems II laboratory
John K. Estell, Thomas A. Owen
March 1995 **ACM SIGCSE Bulletin , Proceedings of the twenty-sixth SIGCSE technical symposium on Computer science education SIGCSE '95**, Volume 27 Issue 1
Publisher: ACM Press
Full text available: pdf(594.32 KB) Additional Information: full citation, references, citings, index terms

16 System design based on single language and single-chip Java ASIP microcontroller
Sérgio Akira Ito, Luigi Carro, Ricardo Pezzuol Jacobi
January 2000 **Proceedings of the conference on Design, automation and test in Europe**
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17 Session 1: Applications: New directions for integrated circuit cards operating systems
Pierre Paradinas, Jean-Jacques Vandewalle
September 1994 **Proceedings of the 6th workshop on ACM SIGOPS European workshop: Matching operating systems to application needs**
Publisher: ACM Press
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Integrated circuit cards or smart cards are now well-known. Applications such as electronic purses (cash units stored in cards), subscriber identification cards used in cellular telephone or access keys for pay-TV and information highways emerge in many places with millions of users. More services are required by applications providers and card holders. Mainly, new integrated circuit cards evolve towards non-predefined multi-purpose, open and multi-user applications. Today, operating systems imp ...

Keywords: Integrated Circuit Card Applications, Integrated Circuit Card Operating System, Object-Oriented Technologies, Secured method execution

18 PSCP: a scalable parallel ASIP architecture for reactive systems
A. Pyttel, A. Sedlmeier, C. Veith
February 1998 **Proceedings of the conference on Design, automation and test in Europe**

Publisher: IEEE Computer Society
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We describe a Codesign approach based on a parallel and scalable ASIP architecture, which is suitable for the implementation of reactive systems. The specification language of our approach is extended statecharts. Our ASIP architecture is scalable with respect to the number of processing elements as well as parameters such as bus widths and register file sizes. Instruction sets are generated from a library of components covering a spectrum of space/time trade-off alternatives. Our approach featu ...

Keywords: FPGA, application-specific, statechart, modular

19 A strategy for real-time kernel support in application-specific HW/SW embedded architectures
Steven Vercauteren, Bill Lin, Hugo De Man
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20 Incorporating cores into system-level specification
Frank Vahid, Tony Givargis
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